

FIG. 1



FIG. 3 is a block diagram of a Noncoherent Hadamard Sequence Generator. The diagram shows the internal structure of the generator, including the input, processing blocks, and outputs.

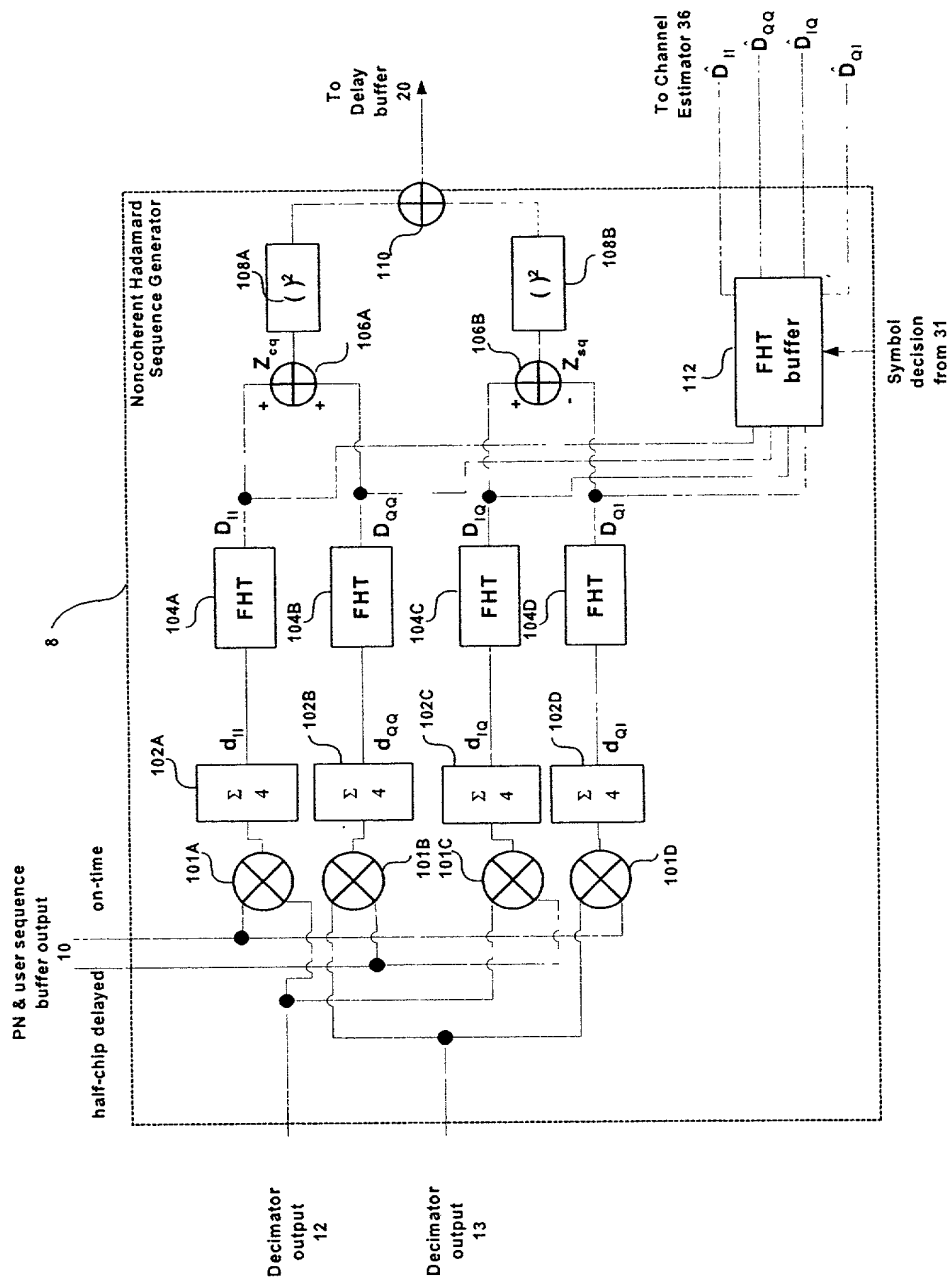


FIG. 3

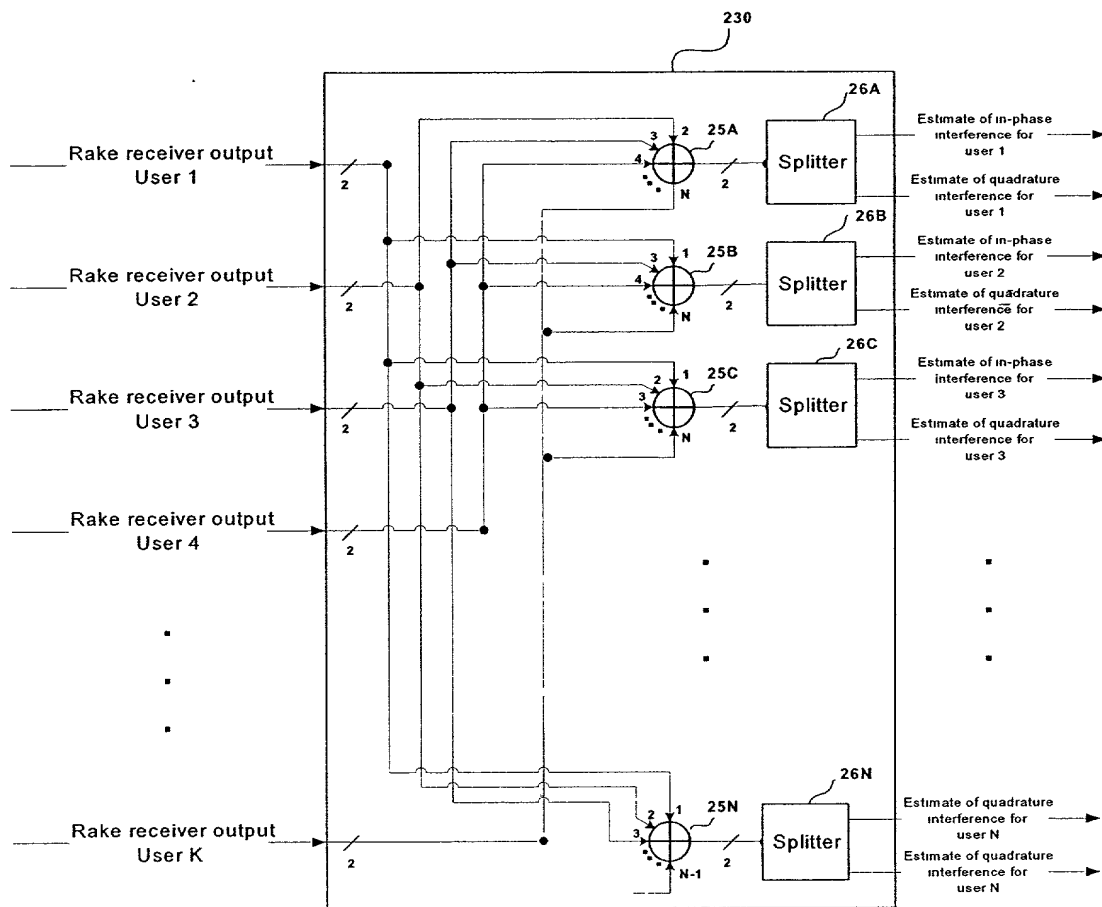


FIG. 4



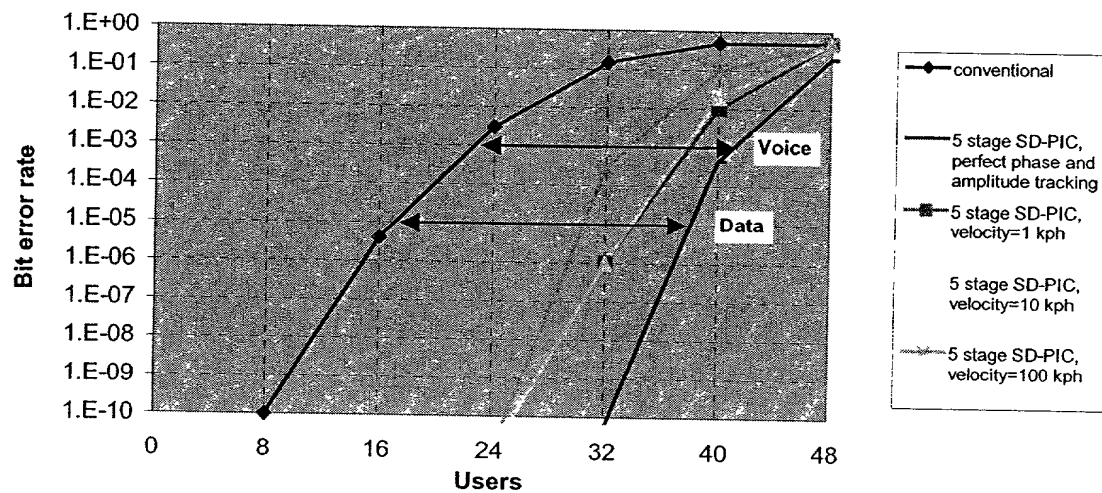
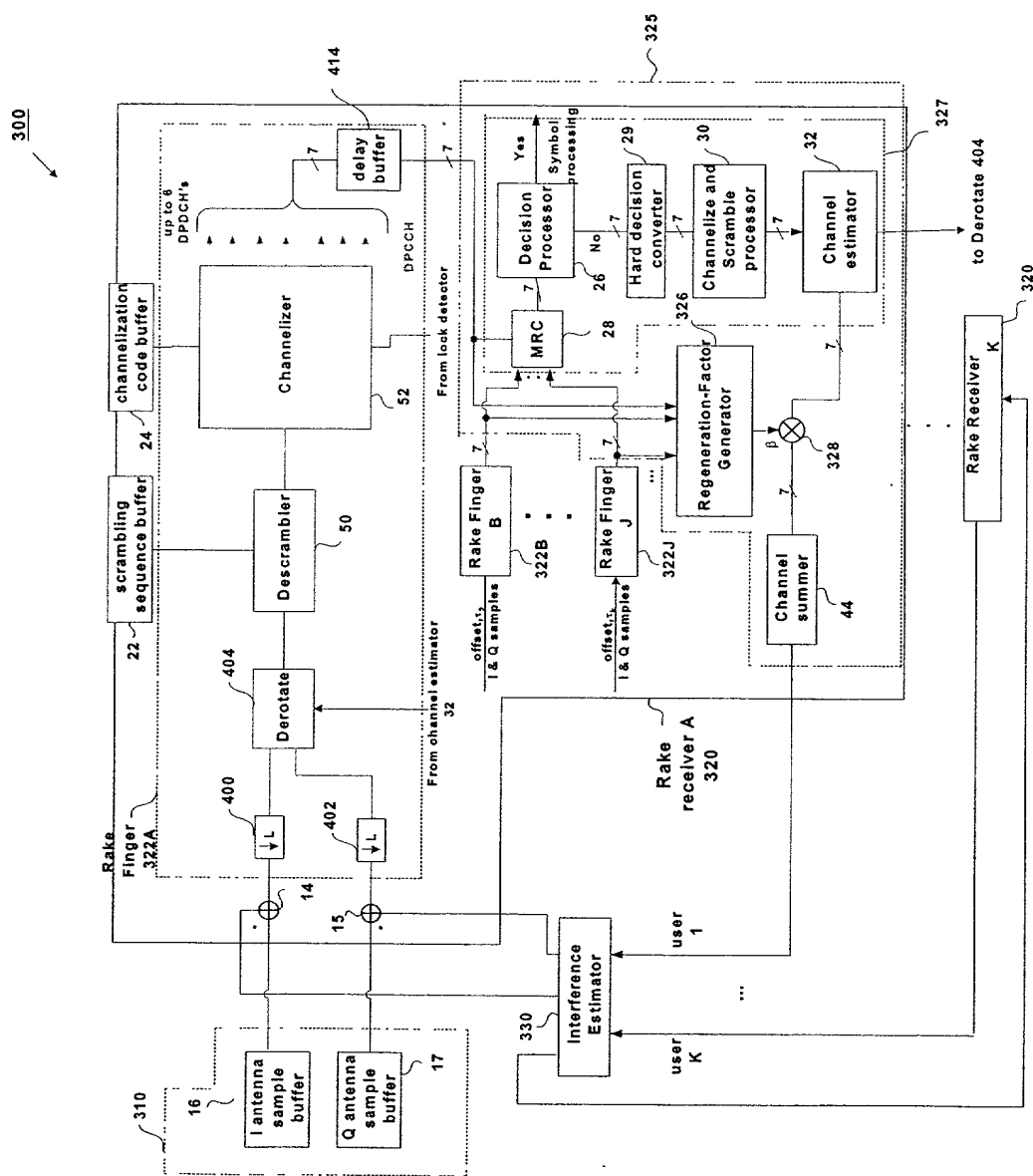


FIG. 6



**FIG. 7**

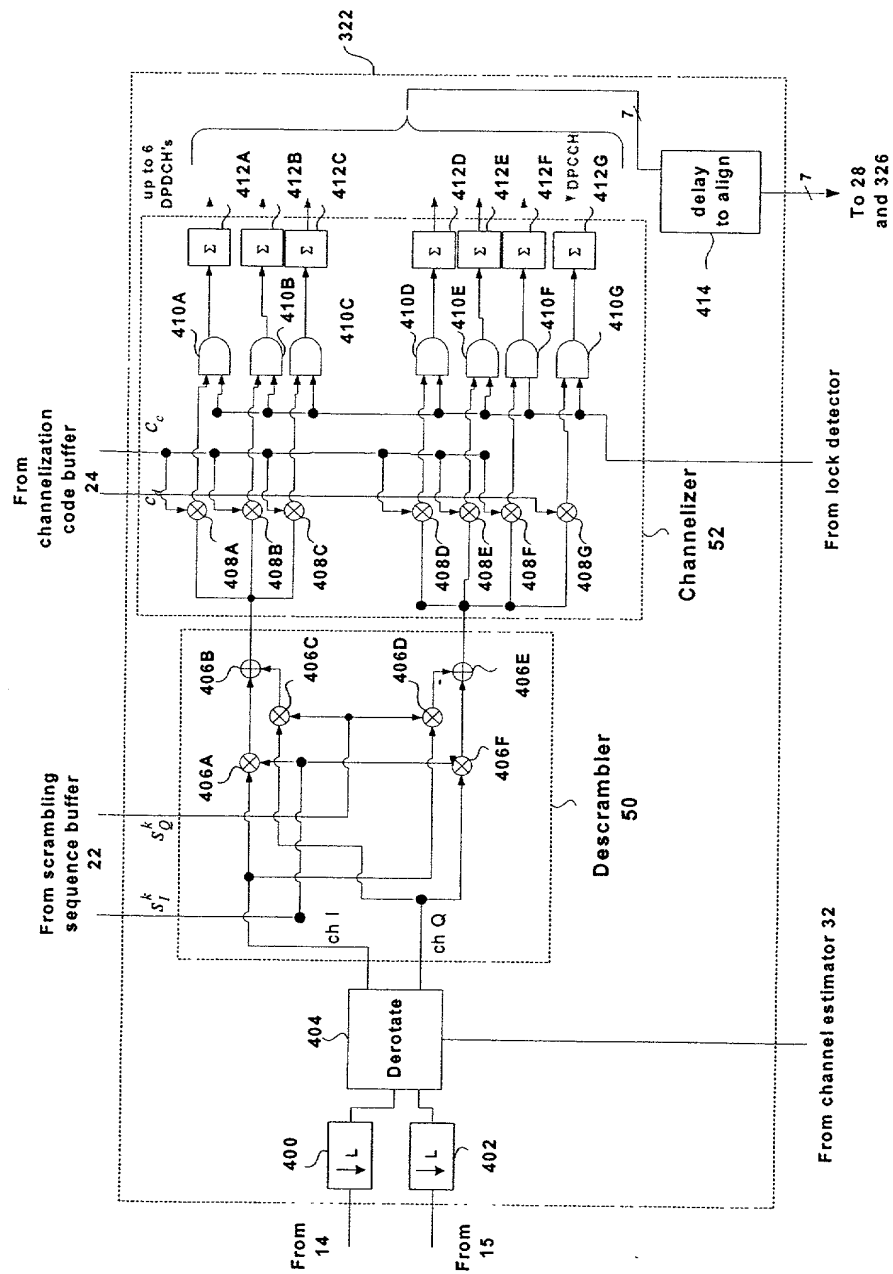


FIG. 8



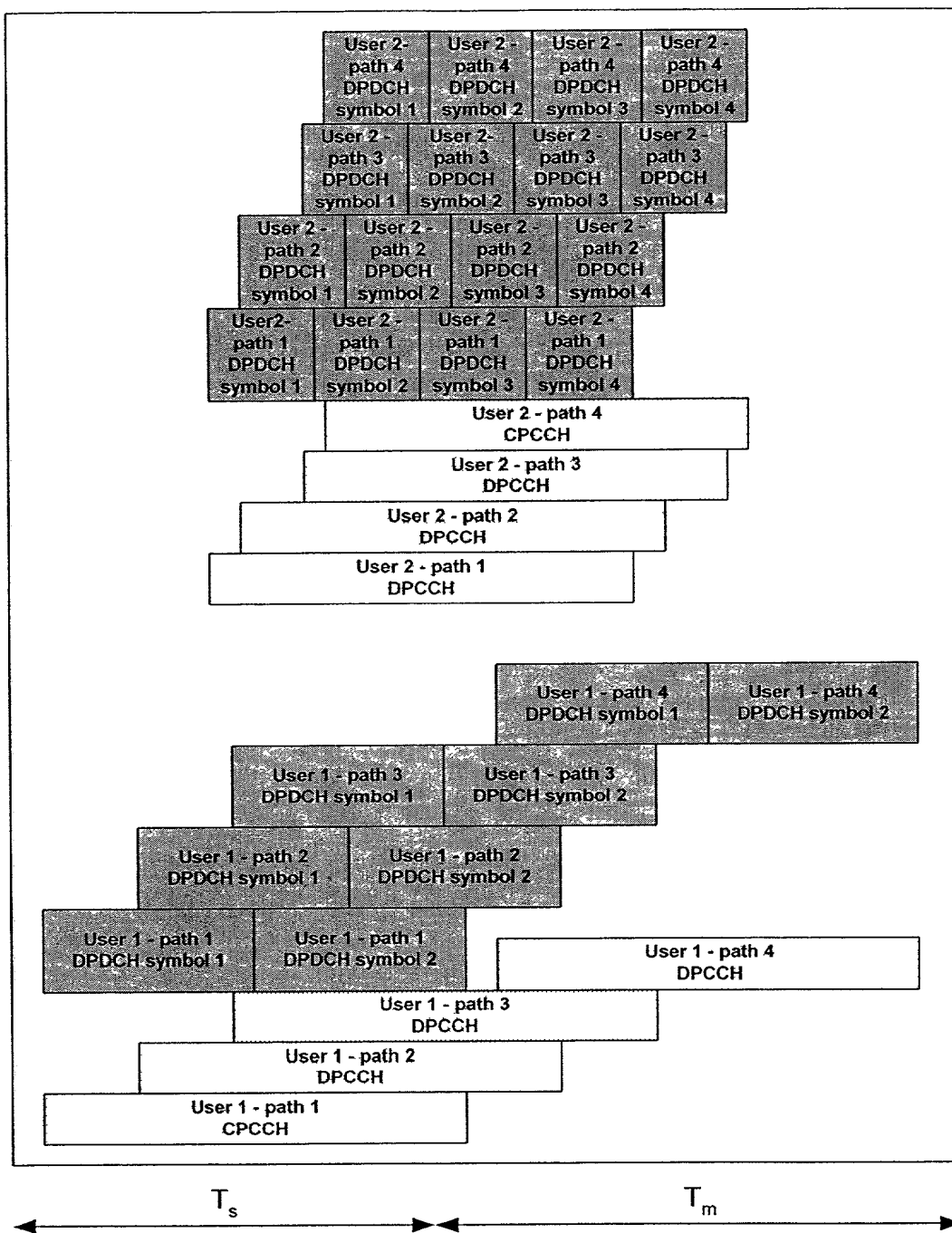
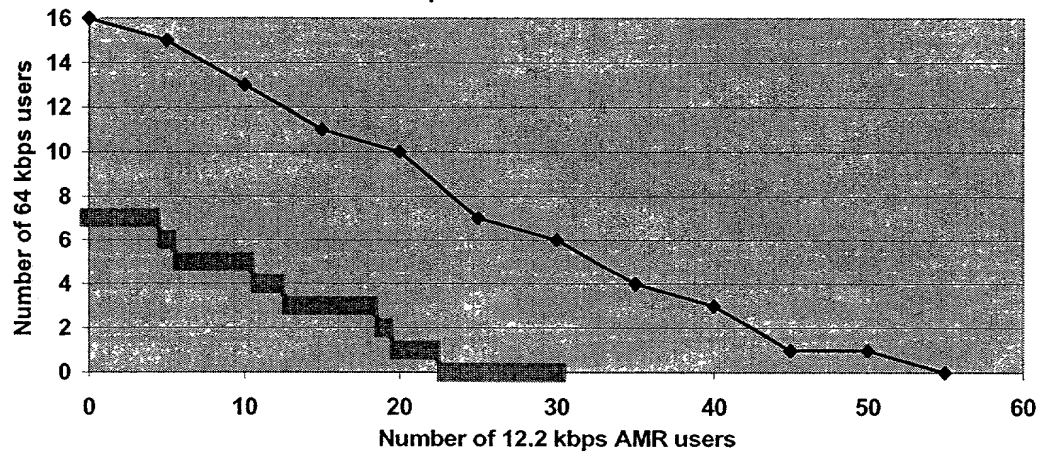


FIG. 9

AMR: 81 Class A bits and 103 Class B bits, R=1/3 conv. coded, 60 Class C bits  
R=1/2 conv. coded ( $E_b/N_0=4$  dB, Class A FER=0.01 required)  
64 kbps: 1408 bits R=1/3 turbo coded ( $E_b/N_0=3$  dB, FER=0.01 required)  
3.4 kbps DCCH and DPCCH included



*FIG. 10*

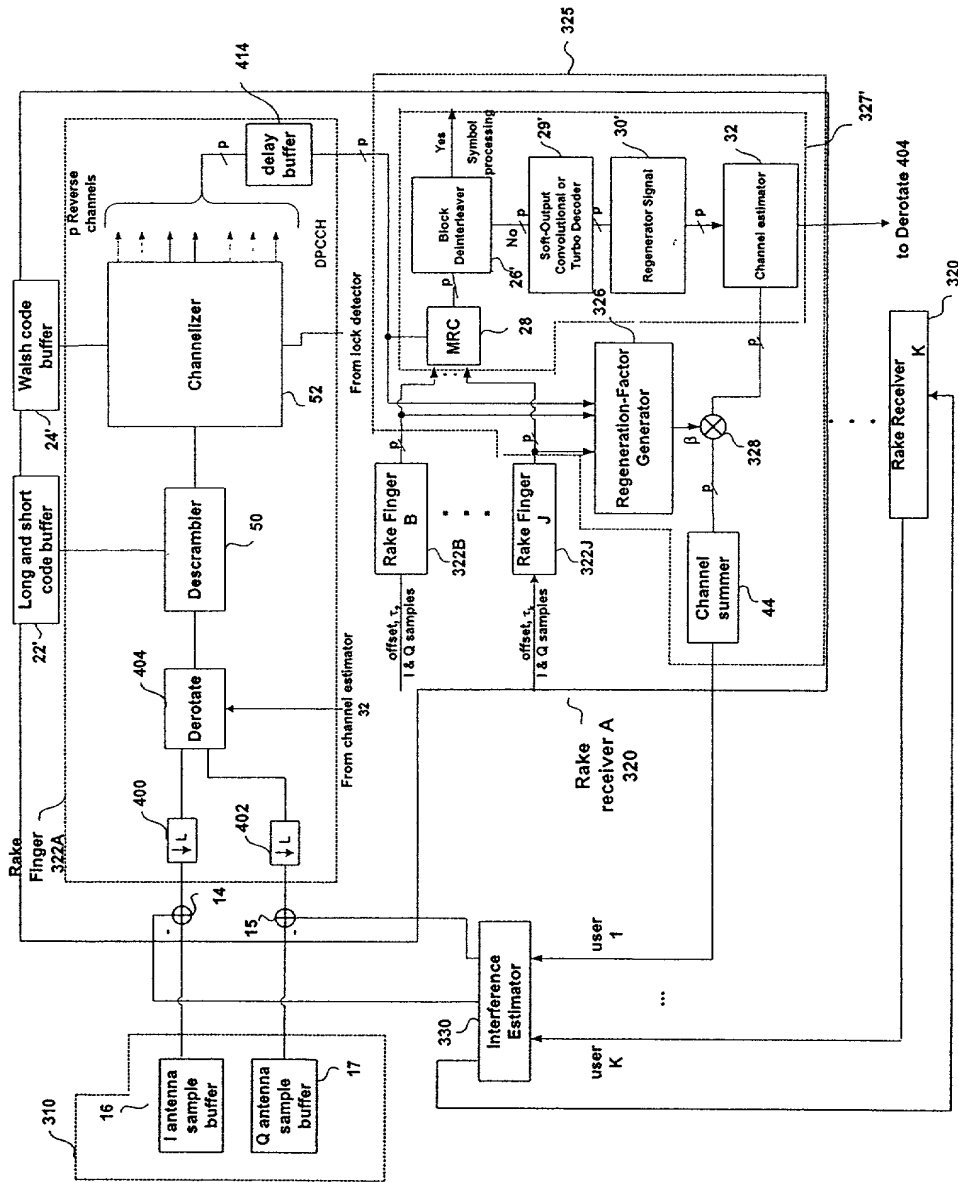


FIG. 11